|  |  |  |
| --- | --- | --- |
| **S. No.** | **Month** | **Contents** |
| 1 | April | Information Representation: Number Systems, Binary Arithmetic, Fixed-point and Floatingpoint representation of numbers, BCD Codes, Error detecting and correcting codes,Character Representation – ASCII, EBCDIC. |
| 2 | May | Binary Logic: Boolean Algebra, Boolean Theorems, Boolean Functions and Truth Tables,Canonical and Standard forms of Boolean functions, Simplification of Boolean Functions –Venn Diagram, Karnaugh Maps.Digital Logic: Basic Gates – AND, OR, NOT, Universal Gates – NAND, NOR, OtherGates – XOR, XNOR etc.**REVISION OF SYLLABUS COVERED YET AND ASSIGNMENT 1** |
| 3 | June | Combinational Circuits: Half-Adder, Full-Adder, Half-Subtractor, Full-Subtractor, Encoders, Decoders, Multiplexers, Demultiplexers, Comparators, Code Converters.Sequential Logic: Characteristics, Flip-Flops, Clocked RS, D type, JK, T type and Master-Slave flip-flops. State table, state diagram. Flip-flop excitation tables**REVISION OF SYLLABUS COVERED YET AND ASSIGNMENT 2 AND SESSIONAL** |
| 4 | July | Shift registers : serial in parallel out and parallel in parallel out.. Designing counters –Asynchronous and Synchronous Binary Counters, Modulo-N Counters and Up-DownCounters.. **Discussion on short questions based on covered chapter Revision of Syllabus** |

**Lesson Plan (Session- 2021-22)**
**B.SC(C.S). – I, Sem. – II**

**Logical Organization of Computers**

**Mr. Naresh, Assistant Professor (Computer Sc.)**

**S U S Govt. College Matak Majri, Indri (Karnal)**